



Use RV1 and SW3 to set the clock speed. Slow provides a frequency of 0.6 - 6 Hz and fast 50 - 800 Hz.

The 8-bit counter output (J7) can be used for other experiments. For example, it can be connected to the ASCII Keyboard Tester (J3 on that board) or One Byte Hex Display (also J3). Make sure SW2 Setting 2 is OFF.

Before inserting either U4 or U5 (not both at the same time), double check voltages on pins accordingly.

DIP switches (SW2) provide these settings:

- 1) ON for for stepping clock using SW1, OFF for automatic clock.
- 2) ON for MOD-8 binary counter (U3A) behavior (use with testing Character Generator ICs), OFF for full 8-bit counting.
- 3) ON turns on the Clock and Row Address LEDs, OFF turns these off.
- 4) ON turns on the latched Character Address LEDs, OFF turns these off.

Revision 1.1:

Add a decoupling capacitor between pin 1 and 8 on the solder side of the PCB. This fixes some erratic display behavior at certain frequency settings.

Connect an Apple-1 (J5) or Apple II (J6) keyboard or use the ASCII Data In (J4).